REMARKS

The office action indicates that the Examiner construes the write-back and write-through caches in a certain way indicated on the bottom of page 10 of the office action. There, it is indicated that a write-back cache "is when data is written to the cache and transferred to the main memory when absolutely necessary." A write-through cache "is when the data is written to both the cache and to the main memory."

The question is does Watanabe change between these two modes?

All that happens in response to the detection of a high temperature in Watanabe is that either the cache is not used and everything is written directly to optical memory or the cache is not used and everything in the write cache is written to optical memory. This does not meet either the definition of the write-back cache or a write-through cache posed by the Examiner. Therefore, it is impossible that the reference transitions between one or the other of the two modes since, in response to temperature, it assumes a mode different from both a write-back and a write-through cache.
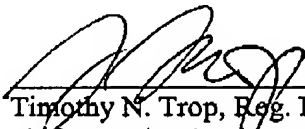
Therefore, reconsideration is respectfully requested.

The informality noted in paragraph 1 on page 2 of the office action has been corrected by this response.

In view of these remarks, the application should now be in condition for allowance.

Respectfully submitted,

Date: December 2, 2005

Timothy N. Trop, Reg. No. 28,994
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Ste. 100
Houston, TX 77024
713/468-8880 [Phone]
713/468-8883 [Fax]

Attorneys for Intel Corporation

9